

Notice of References Cited

Application/Control No.

09/631,628

Applicant(s)/Patent Under
Reexamination
KATO ET AL.

Examiner

Helen B Rossoshek

Art Unit

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,633,945	10-2003	Fu et al.	710/316
	B	US-4,987,326	01-1991	Satoh et al.	326/43
	C	US-5,585,210	12-1996	Lee et al.	430/5
	D	US-5,742,180	04-1998	DeHon et al.	326/40
	E	US-5,998,270	12-1999	Gilmer et al.	438/287
	F	US-5,646,516	07-1997	Tobita	323/313
	G	US-5,701,507	12-1997	Bonneau et al.	716/8
	H	US-5,339,396	08-1994	Muramatsu et al.	710/317
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	IBM, "Optimized Design of an integrated MSI Thyristor crosspoint", May 1981, IBM Technical disclosure bulletin, Volume number 23, Issue number 12, Page number 5412
	V	Frailong et al., "The next-generation SPARC multiprocessing system architecture", Feb. 1993, Compcon Spring '93, Digest of Papers, 22-26, Page(s): 475-480
	W	Melnyk et al., "Scalable parametrizable SMP system core architecture", Feb. 2001, CADSM 2001. The Experience of Designing and Application of CAD Systems in Microelectronics. Proceedings of the VI-th International Conference, 12-17 Feb. 2001,
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.